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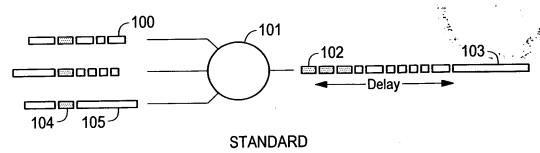


FIG. 2A

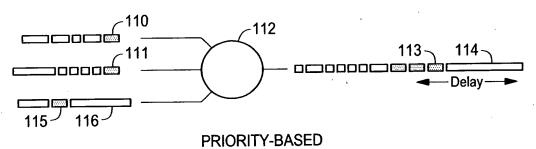


FIG. 2B

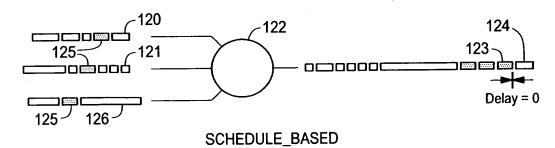
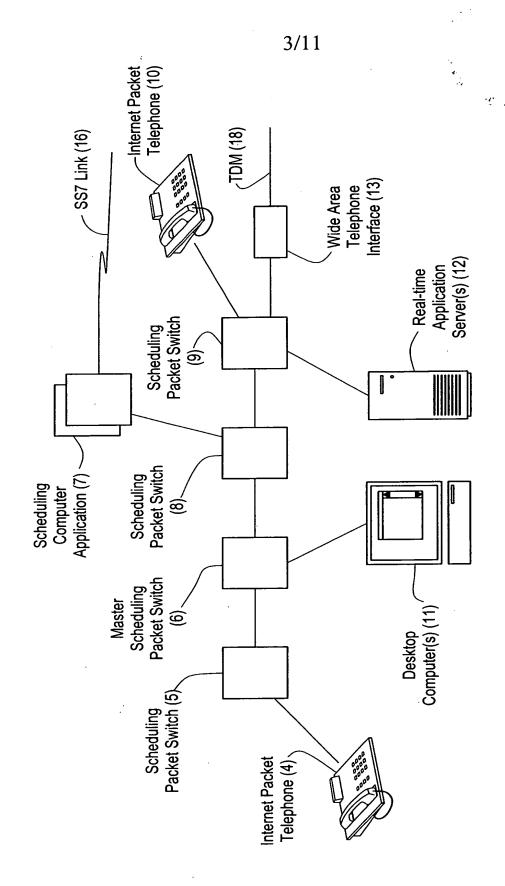
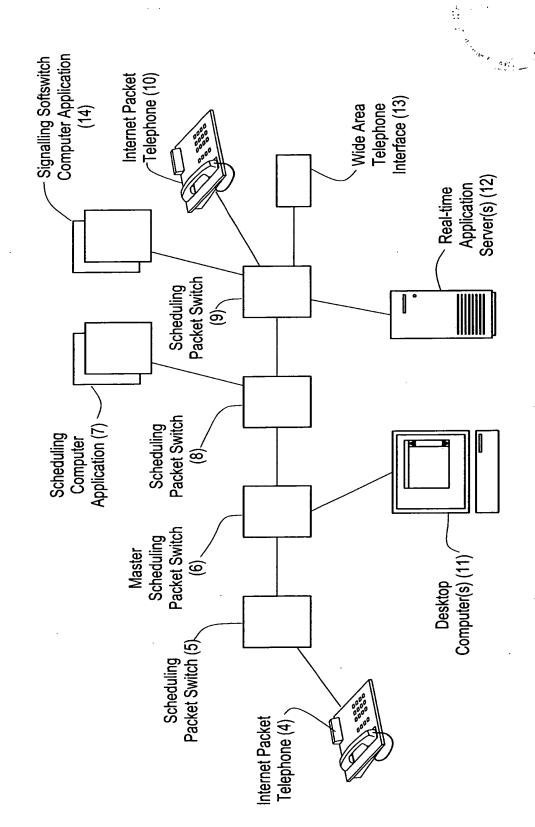


FIG. 2C

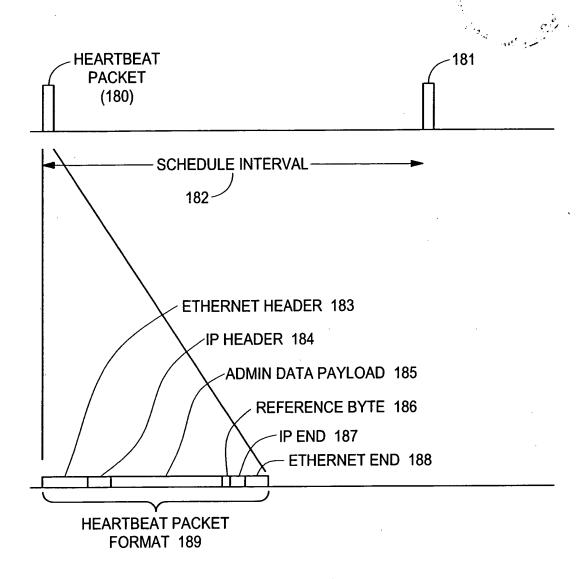


Network of Scheduled Switches FIG.3



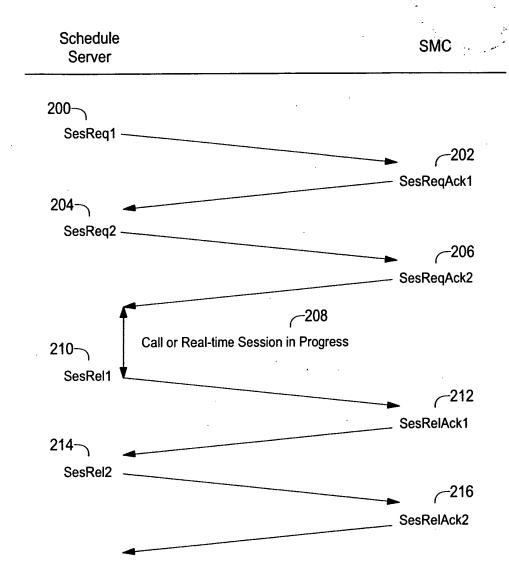
Network of Scheduled Switches with Signalling Softswitch

FIG. 4



HEARTBEAT PACKET STRUCTURE

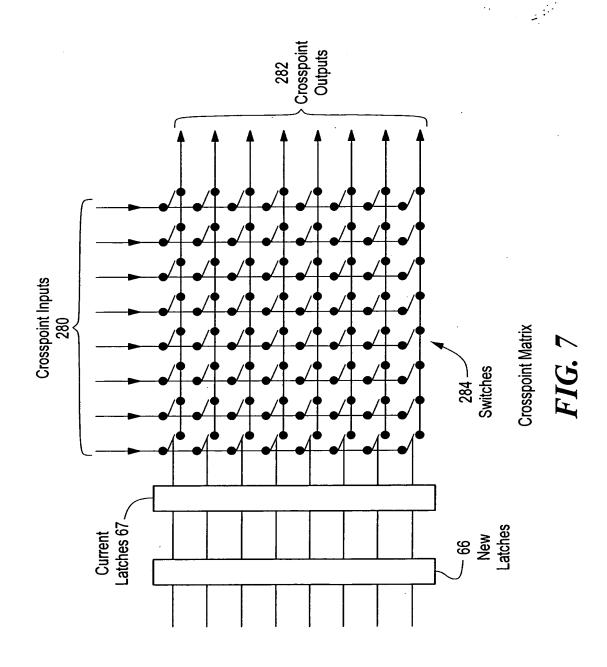
FIG. 5

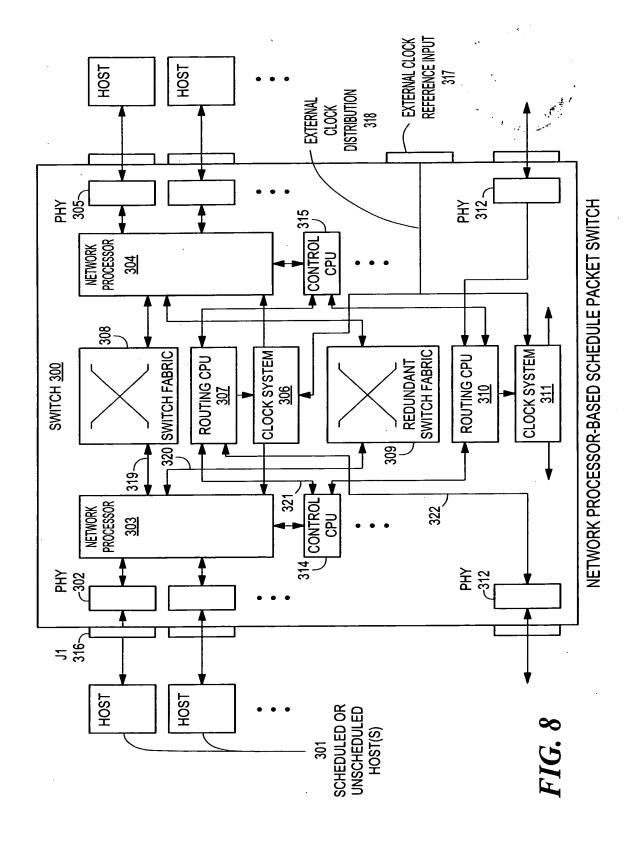


Protocol Messages for a Bi-directional Real-time Session Establishment and Termination

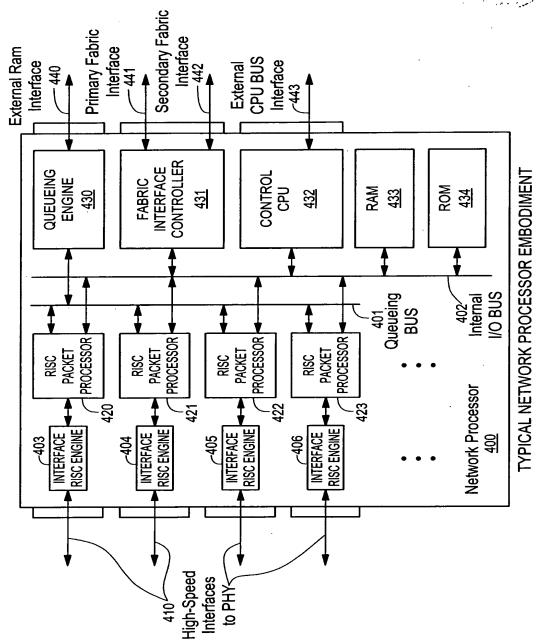
FIG. 6





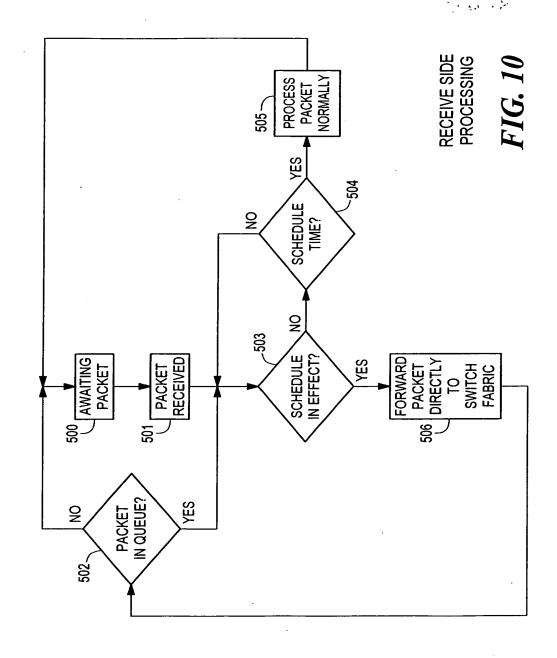


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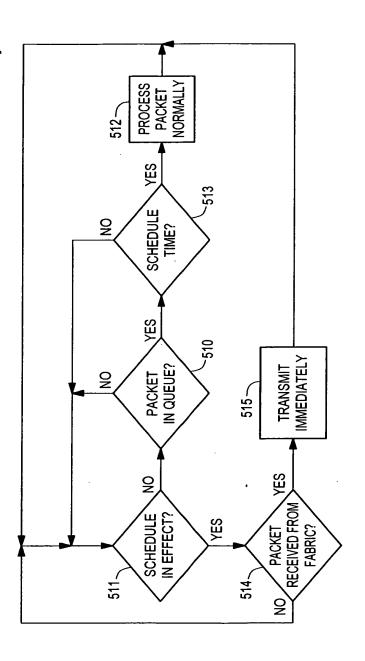


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FIG. 9



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TRANMIT SIDE PROCESSING

FIG. 11